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VIAP0020USA

METHOD AND RELATED CIRCUIT FOR CLOCK GENERATION AND RECOVERY

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APP_ID=10064053

Page 1 of 5

30-06-'05 16:42 從 WINSTON HSU, NO. 41526 8064986673

T-260 P18/21 U-252

Attached Files:

declaration	VIAPO020DEC1.TIF
declaration	VIAPO020DEC2.TIF
specification	VIAPO020.xml
bibd-transmittal	VIAPO020apds.xml
patent-assignments	VIAPO020asgn.xml
fee-transmittal	VIAPO020fee.xml

Attached Image File(s):

VIAPO020DEC1.TIF
VIAPO020DEC2.TIF

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APP_ID=10064053

Page 2 of 5

30-06-'05 16:43 從- WINSTON HSU, NO. 41526 8064986673

T-260 P19/21 U-252

Comments:

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Page 3 of 5